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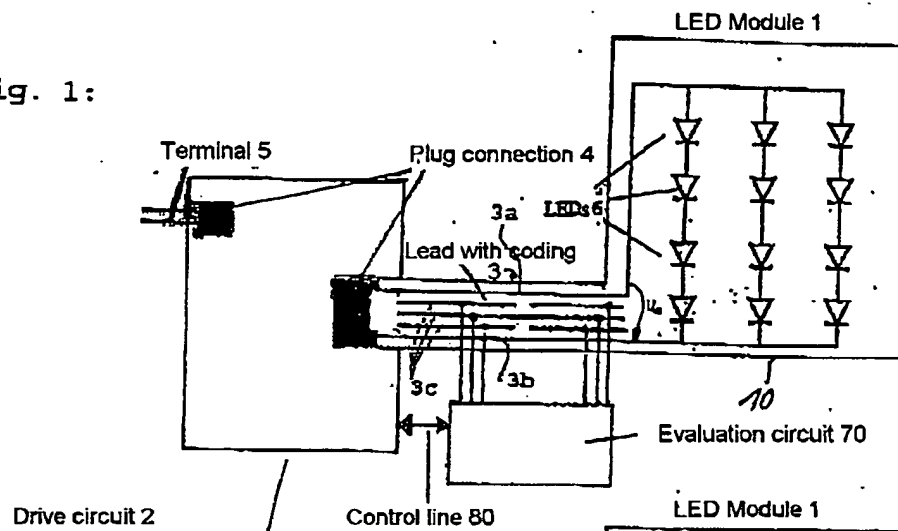


Fig. 2:

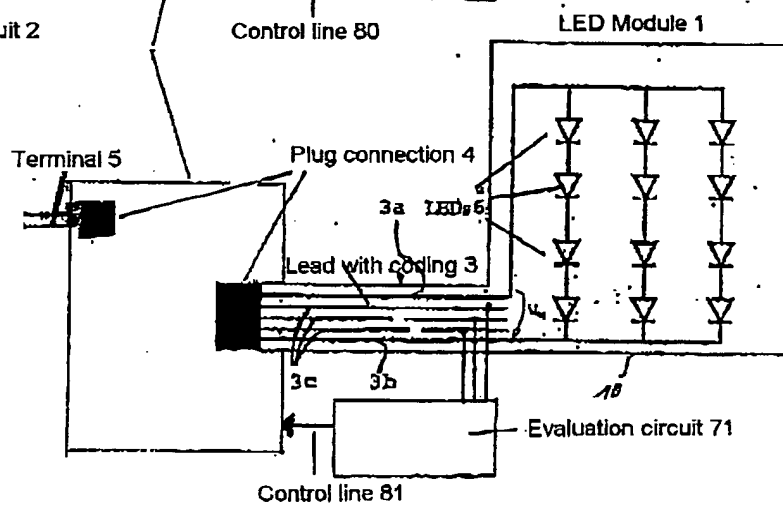


Fig. 3:

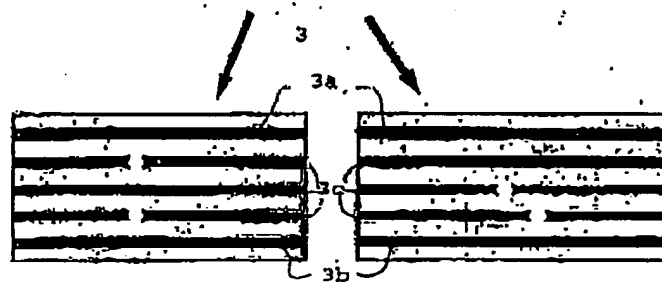


Fig. 4:

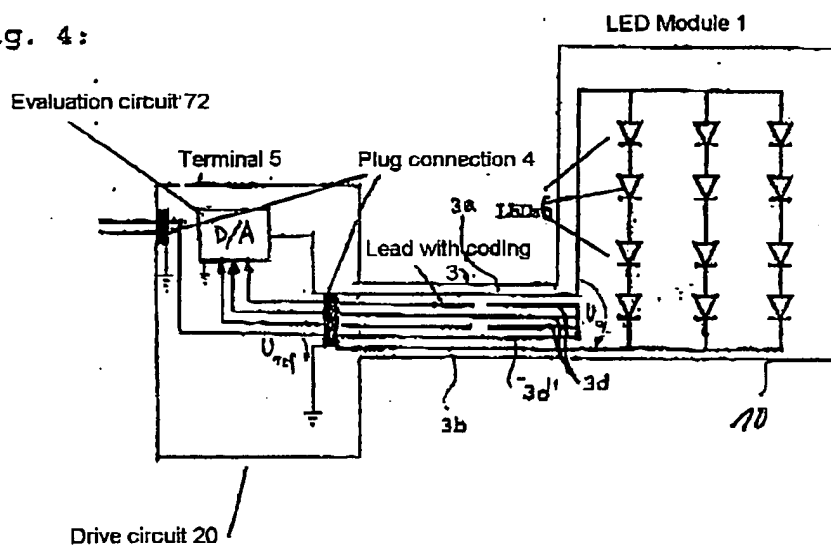


Fig. 5:

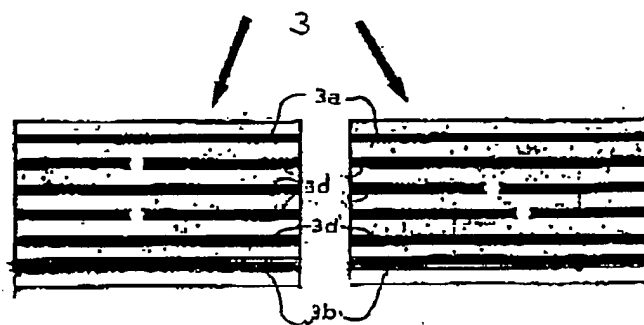


Fig. 6:

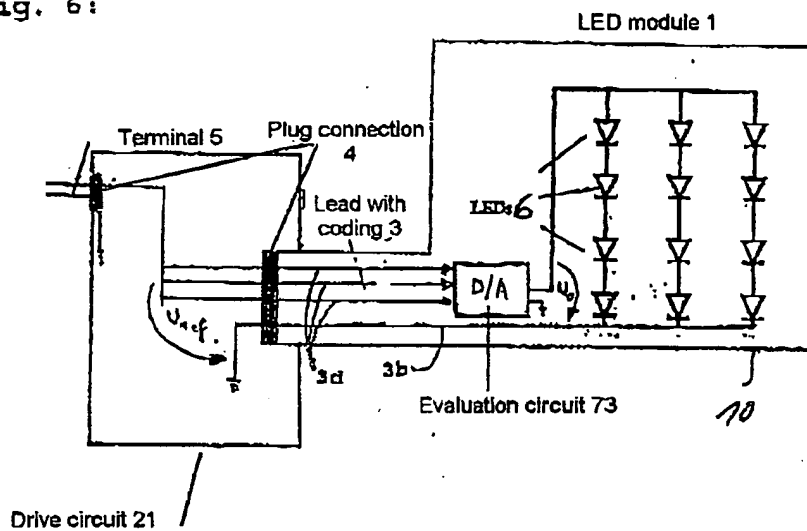


Fig. 7:

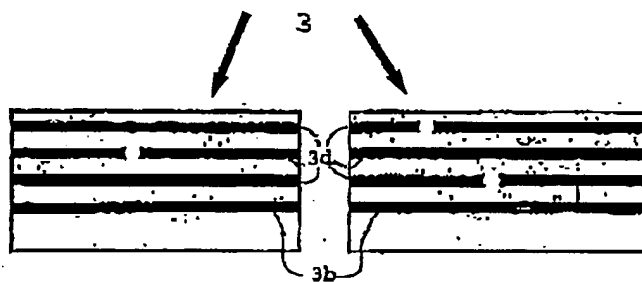


Fig. 8:

Evaluation circuit 72; 73

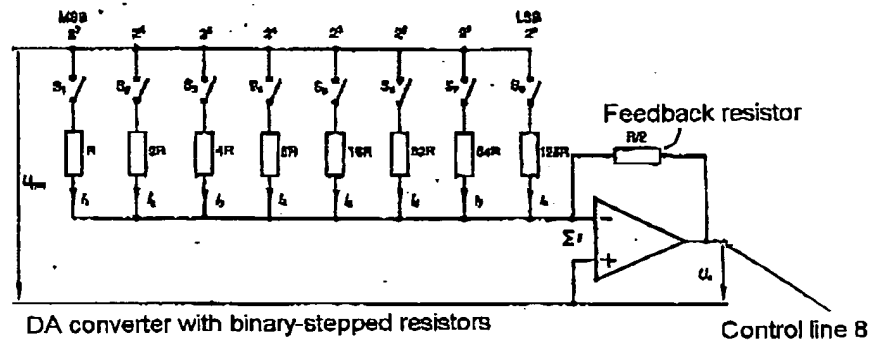


Fig. 9:

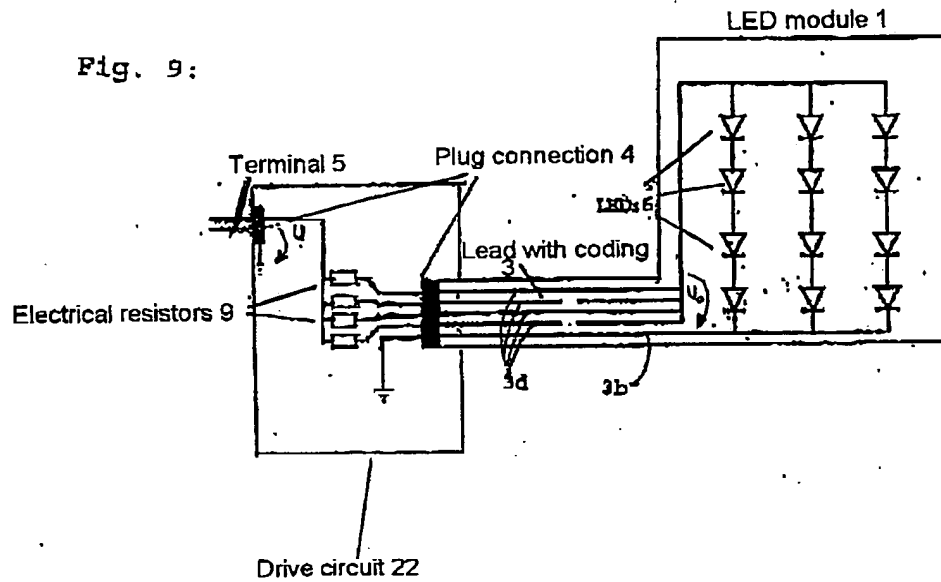


Fig. 10:

